

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Xiaoju Wu, et al.

Serial No:

10/017,990

Examiner:

Dana Farahani

Filed:

12/14/2001

For:

BIPOLAR JUNCTION TRANSISTOR WITH ELECTRICAL HOLE ISOLATOR

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS

Mail Stop AF **Commissioner For Patents** P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA

Docket No:

Conf. No:

Art Unit:

TI-31214

8404

2814

22313-1450 on <u>6-12-03</u>

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed February 12, 2003, finally rejecting Claims 1-30.

		The item(s) checked below are appropriate:			ECH	į	
	1.		An extension of time to respond to the final rejection		ECHNOLOGY	JU	
				was granted on is requested for month.	CEA	B	
	2.	<u>X</u>	A time	ely response to the final rejection has been filed.	TER:	2003	
	3.	<u>X</u>	Fee \$	\$320.00 :			
			<u>X</u>	Not required (Fee paid in prior appeal) The Commissioner of Patents is hereby authorized to charge fees which may be required or credit any overpayment to the account of Texas Instruments Incorporated, Account No. 20-0 This form is submitted in triplicate.			
GS/17/2003	HDAHTE1	0000009	200668	10017990 ()			
91 FC:1401		320.00 DA		KAM		_	

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-4258

\ Peter K. McLarty Attorney for Appellants Reg. No. 44,923